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** CONTINUING DATA *****
NONE

** FOREIGN APPLICATIONS *****
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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>7/1</i> Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 10	TOTAL CLAIMS 15	INDEPENDENT CLAIMS 3
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TITLE
 TRANSISTOR, INTEGRATED CIRCUIT, ELECTRO-OPTIC DEVICE, ELECTRONIC INSTRUMENT AND METHOD OF MANUFACTURING A TRANSISTOR

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